

Abstract of the Disclosure

An interrupt delivery system is provided for delivering interrupt requests in a multi-node computer system. The interrupt delivery system includes a pair of scaleable node controllers, each of which supports at least one microprocessor. The scaleable node controllers are coupled to a scalability port switch, which receives an interrupt request. The scalability port switch determines an address of one of the scaleable node controllers from the interrupt request. Based on the address, the scalability port switch then transmits the interrupt request to the correct scaleable node controller.

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